

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10001477
Filing Date	2001-11-01
First Named Inventor	Steve Roe
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01203M

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/JP/	1	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages	<input type="checkbox"/>
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/JP/	1	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Monte Mar, filed on 12/09/1998; 23 pages	<input type="checkbox"/>
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/JP/	3	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages	<input type="checkbox"/>
/JP/	4	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Perrin et al., filed on 09/09/2002; 41 pages	<input type="checkbox"/>

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/JP/	1	USPTO Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit," Monte Mar, filed on 08/22/2001; 51 pages 1 page	<input type="checkbox"/>
/JP/	2	USPTO Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit," Warren Snyder, filed on 08/06/2001; 25 pages	<input type="checkbox"/>
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/JP/	8	USPTO Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller," Kutz et al., filed on 06/22/2001; 42 pages	<input type="checkbox"/>
/JP/	9	USPTO Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot," Nemecek et al., filed on 11/01/2001; 44 pages	<input type="checkbox"/>
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/JP/	17	USPTO Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller," Kutz et al., filed on 05/09/2005; 1 page	<input type="checkbox"/>
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/JP/	22	USPTO Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode," Warren Snyder, filed on 10/05/2001; 30 pages	<input type="checkbox"/>

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/JP/	23	USPTO Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface," Warren Snyder, filed on 10/05/2001; 32 pages	<input type="checkbox"/>
/JP/	24	USPTO Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability," Mar et al., filed on 09/19/2001; 28 pages	<input type="checkbox"/>
/JP/	25	USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Suliam et al., filed on 10/25/2001; 49 pages	<input type="checkbox"/>
/JP/	26	USPTO Application Number 09/972,319 (CD00179): "Method for Applying Instructions to Microprocessor in Test Mode," Warren Snyder, filed on 10/05/2001; 31 pages	<input type="checkbox"/>

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/JP/	1	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; 4 pages	<input type="checkbox"/>
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/JP/	1	In circuit "In Circuit Emulators - descriptions of the major ICs around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages	<input type="checkbox"/>

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/JP/	2	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages	<input type="checkbox"/>
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/JP/	24	6272646	B1	2001-08-07	Rangasayee	Entire Document
/JP/	25	6560306	B1	2003-05-06	Duffy	Entire Document
/JP/	26	6667642	B1	2003-12-23	Moyal	Entire Document
/JP/	27	6369660	B1	2002-04-09	Wei	Entire Document
/JP/	28	6377646	B1	2002-04-23	Sha	Entire Document
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/JP/	31	6075941	B1	2000-06-13	Itoh et al.	Entire Document
/JP/	32	6289300	B1	2001-09-11	Brannick et al.	Entire Document
/JP/	33	6298320	B1	2001-10-02	Buckmaster et al.	Entire Document
/JP/	34	6032268	B1	2000-02-29	Swoboda et al.	Entire Document
/JP/	35	6347395	B1	2002-02-12	Payne et al.	Entire Document
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/JP/	1	USPTO Application Number 10/033,027 (CD00232M): "Microcontrollable Programmable System on a Chip," Warren Snyder; filed on 10/22/2001; 117 pages	<input type="checkbox"/>
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1	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE, 8 pages DATE		<input type="checkbox"/>
/JP/ 2	USPTO Application Number 09/989,815 (CD01166M): "A Data Driven Method and System for Monitoring Hardware Resource Usage for Programming an Electric Device," Bartz et al., filed on 11/19/2001; 36 pages		<input type="checkbox"/>
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/JP/ 6	USPTO Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress," Craig Nemecek, filed on 11/15/2001; 33 pages		<input type="checkbox"/>
/JP/ 7	USPTO Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between an ICE and a Production Microcontroller while in a Halt State," Craig Nemecek, filed on 11/15/2001; 33 pages		<input type="checkbox"/>
/JP/ 8	USPTO Application Number 10/113,065 (CD01215M): "System and Method for Automatically Matching Components in a Debugging System," Nemecek et al., filed on 03/29/2002; 32 pages		<input type="checkbox"/>
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/JP/	1	USPTO Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes," Wright et al., filed on 09/05/2007; 33 pages	<input type="checkbox"/>
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/JP/	1	USPTO Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants," Gehring et al.; filed on 08/22/2002; 32 pages	<input type="checkbox"/>
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/JP/	12	USPTO Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al., filed on 12/22/2000; 9 pages	<input type="checkbox"/>
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/JP/	10	Burgos et al. Burgos et al. "Power Converter Analysis and Design using Matlab: A Transfer Function Approach," Proceedings of IEEE International Symposium on Industrial Electronics, 1998, Vol. 2; 6 pages	<input type="checkbox"/>
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/JP/	1	"The Gemini Netlist Comparison Project;" < http://www.cs.washington.edu/research/projects/lis/www/gemini/gemini.html > larry@cs.washington.edu; 3/19/2002; 2 pages	<input type="checkbox"/>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)		Application Number	10001477
		Filing Date	2001-11-01
		First Named Inventor	Steve Roe
		Art Unit	2123
		Examiner Name	Jason Scott Proctor
		Attorney Docket Number	CD01203M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/JP/	2	6149299	B1	2000-11-21	Aslan et al.	Entire Document
/JP/	3	6008685	B1	1999-12-28	Kunst, David J.	Entire Document
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Examiner Initial*	Cite No	Publication Number	Kind Code†	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/JP/	1	DE 19710829	DE	A1	1998-09-24	Goloub, Boris	Entire Document	NO <input type="checkbox"/>

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NON-PATENT LITERATURE DOCUMENTS

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/JP/	1	USPTO Non-Final Rejection for Application Number 11/865,672 (CD2007098) dated 07/17/2009; 6 pages	<input type="checkbox"/>
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/JP/	6	USPTO Non-Final Rejection for Application Number 12/104,672 (CD2007138) dated 08/26/2009; 11 pages	<input type="checkbox"/>
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/JP/	8	International Search Report of the International Searching Authority for International Application Number PCT/US08/60695 (CD2007132) dated 07/22/2009; 3 pages	<input type="checkbox"/>
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/JP/	18	USPTO Notice of Allowance for Application Number 11/965,677 (CD2007132) dated 09/10/2009; 6 pages	<input type="checkbox"/>
/JP/	19	USPTO Non-Final Rejection for Application Number 11/965,677 (CD2007132) dated 03/10/2009; 10 pages	<input type="checkbox"/>
/JP/	20	Written Opinion of the International Searching Authority for International Application Number PCT/US08/60696 (CD2007133) dated 09/22/2008; 4 pages	<input type="checkbox"/>

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Please see 37 CFR 1.97 and 1.98 to make the appropriate selection(s):

That each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement. See 37 CFR 1.97(e)(1).

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See attached certification statement.
 Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
 None

SIGNATURE

A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Lucinda G. Price/	Date (YYYY-MM-DD)	2009-11-02
Name/Print	Lucinda G. Price	Registration Number	42270

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1 hour to complete, including gathering, preparing and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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